

FIG. 1

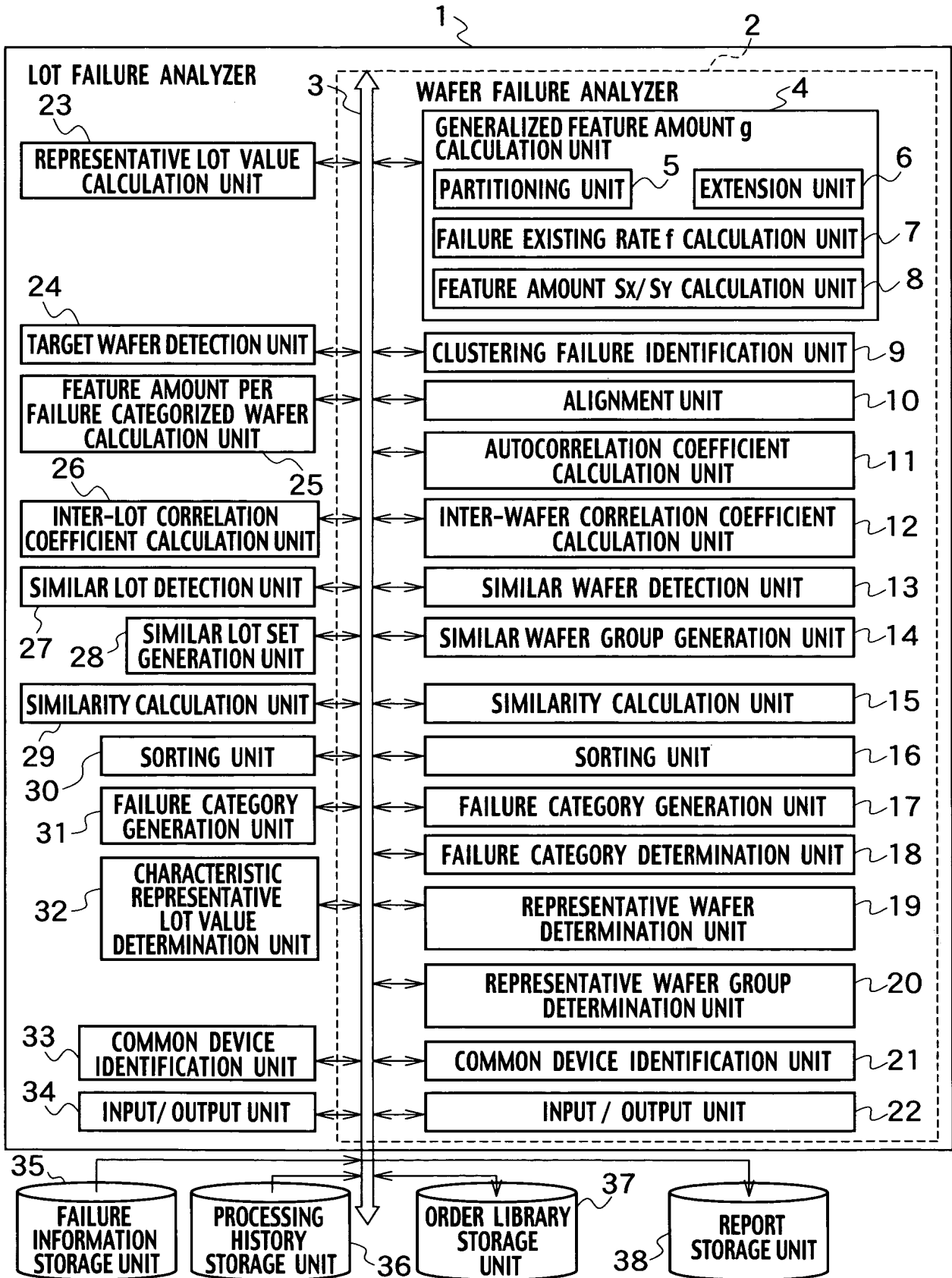


FIG. 2

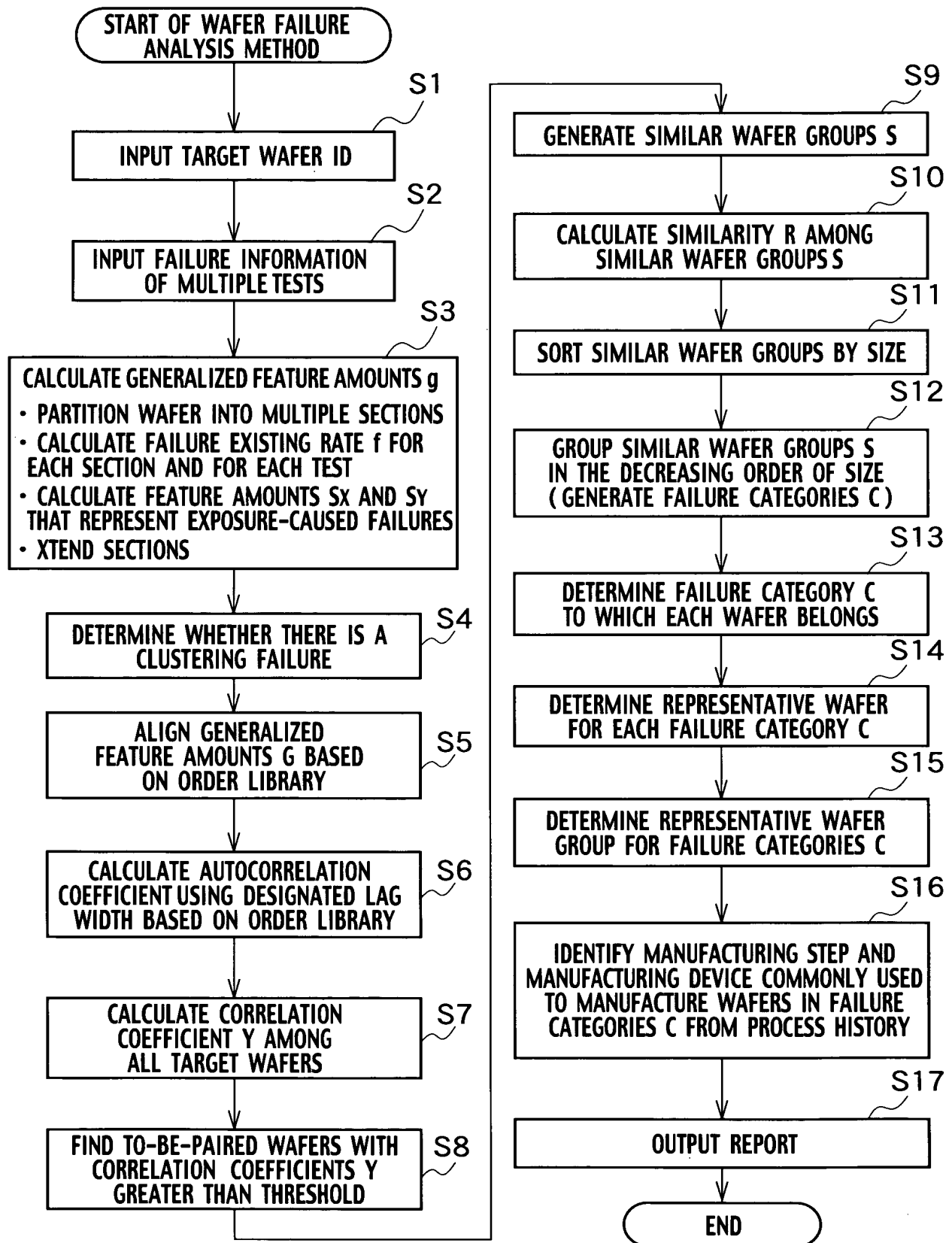


FIG. 3

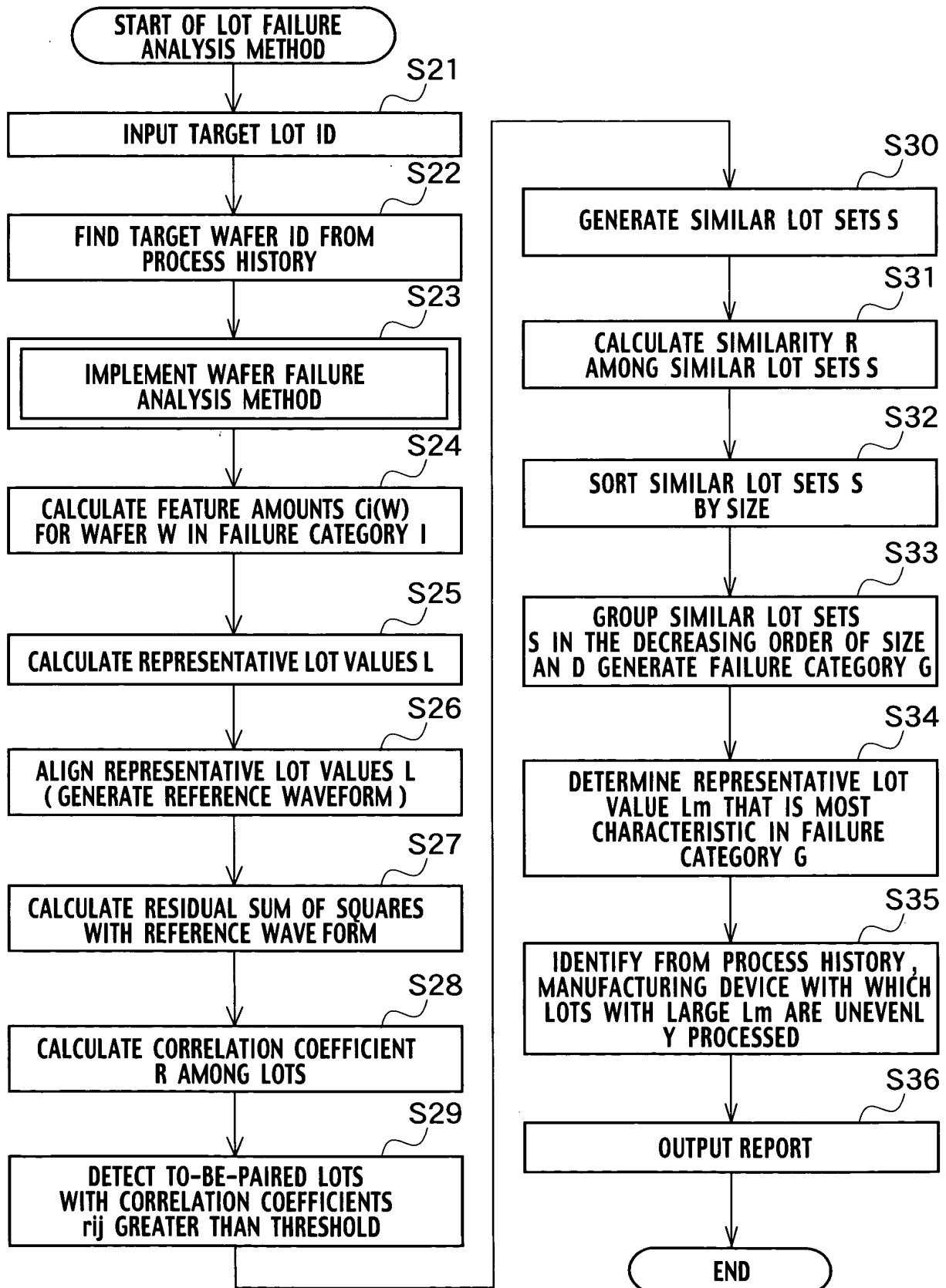


FIG. 4

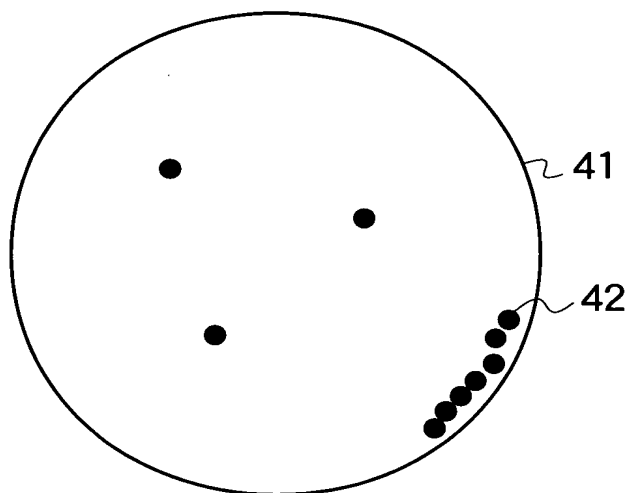


FIG. 5

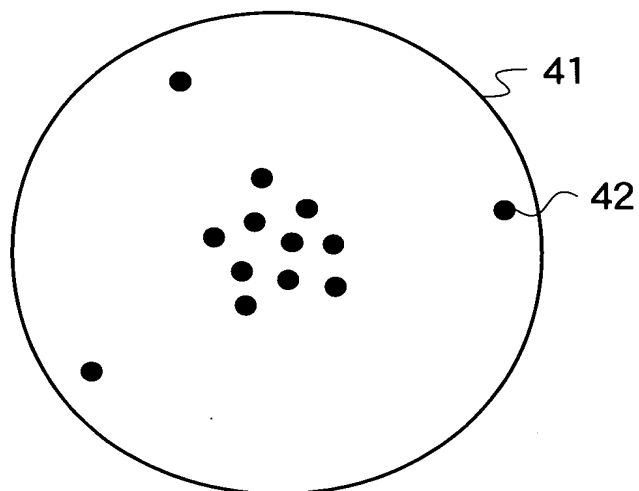


FIG. 6

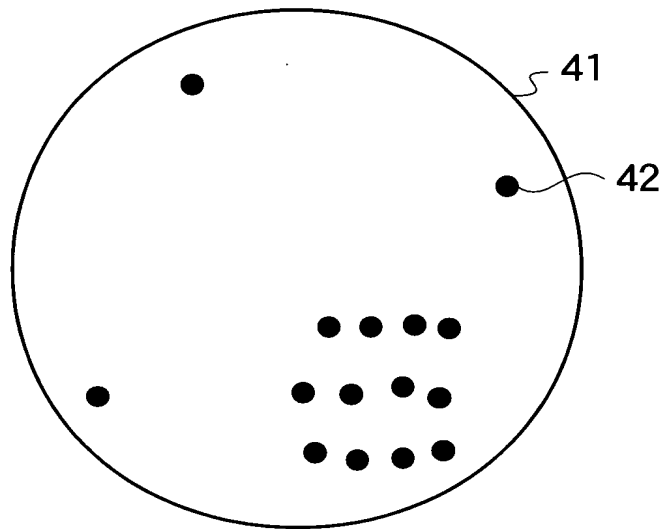


FIG. 7

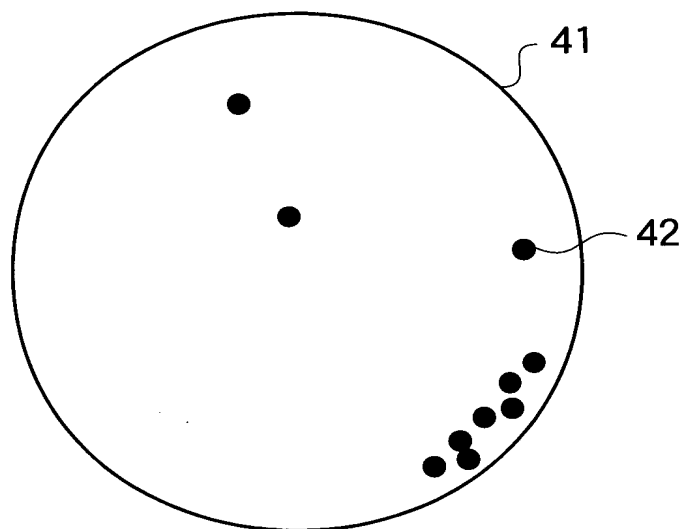


FIG. 8

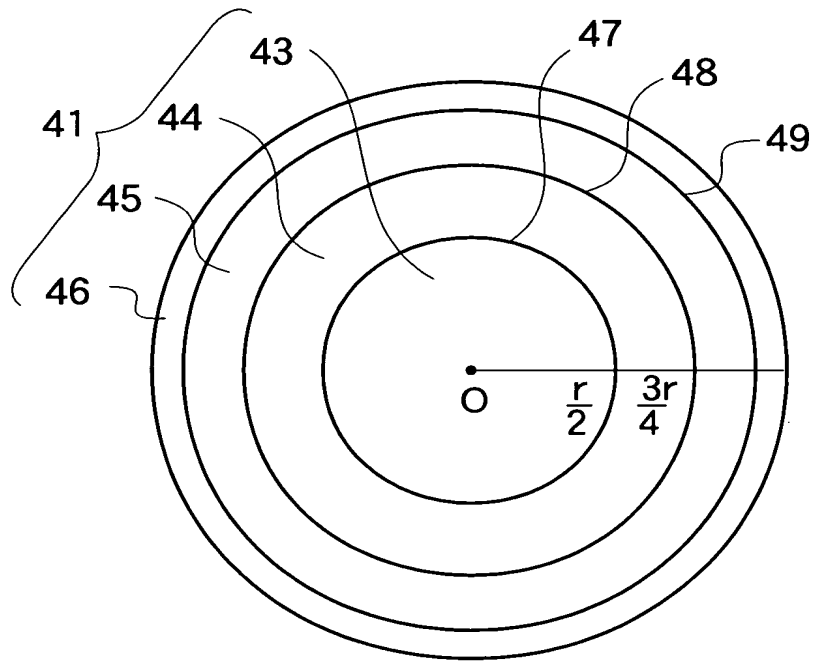


FIG. 9

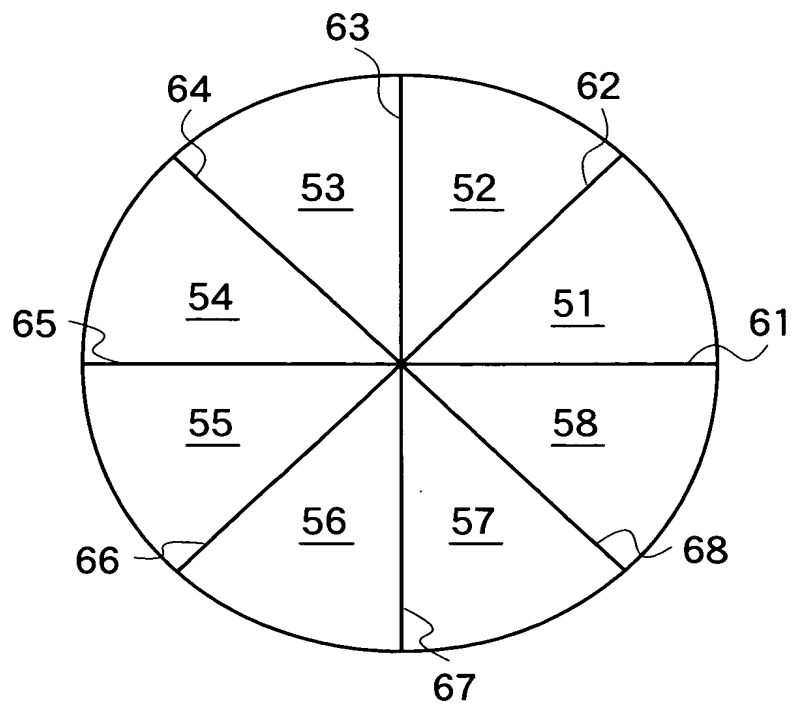


FIG. 10

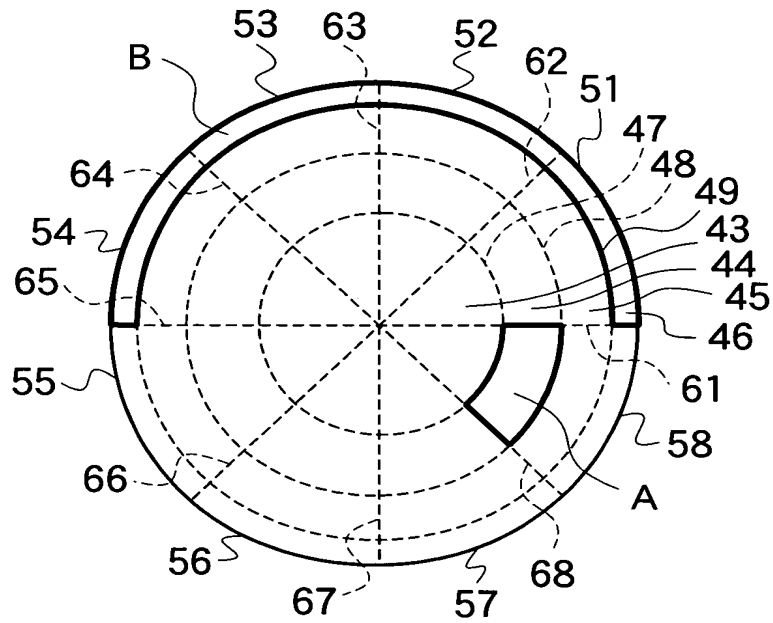


FIG. 11

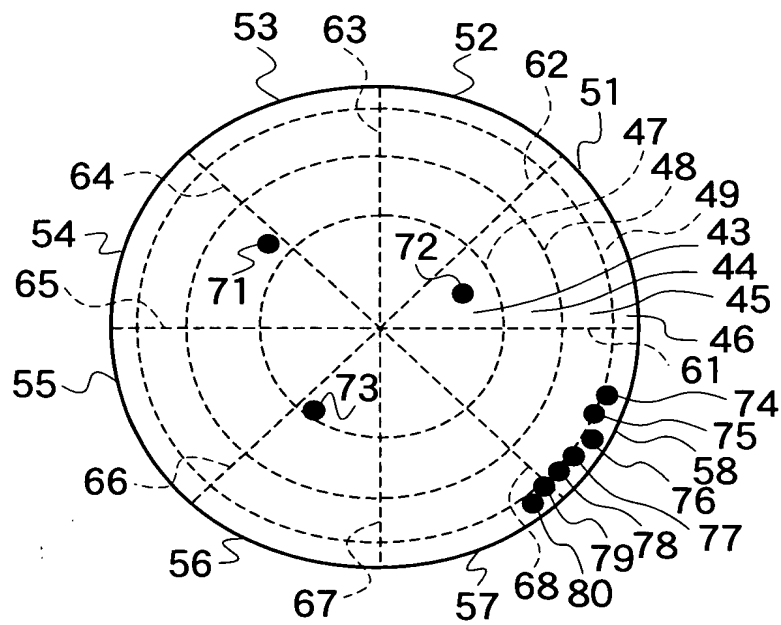


FIG. 12A

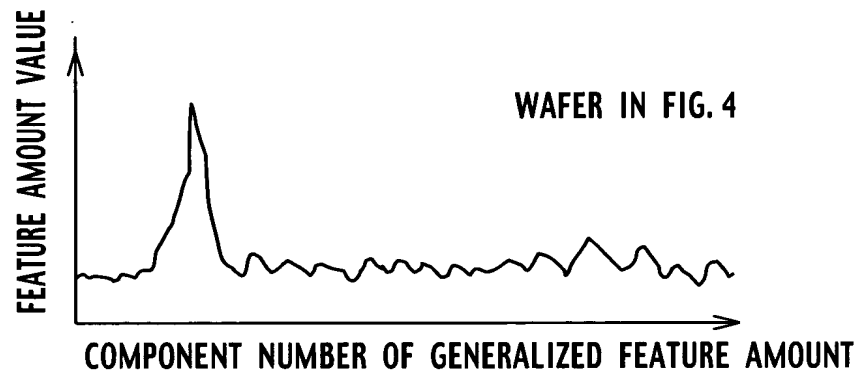


FIG. 12B

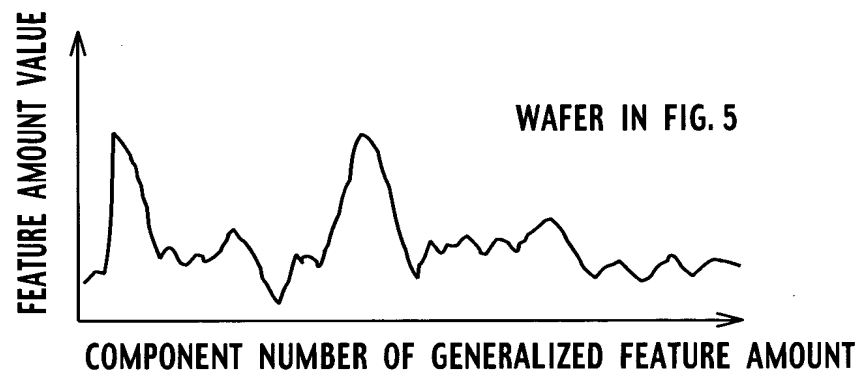


FIG. 12C

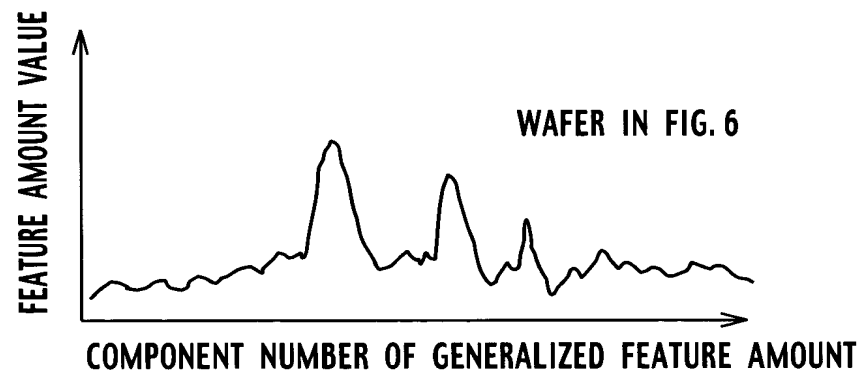


FIG. 12D

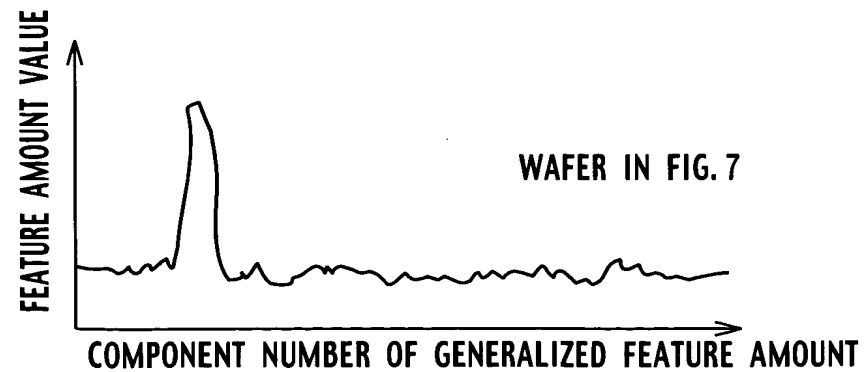


FIG. 13

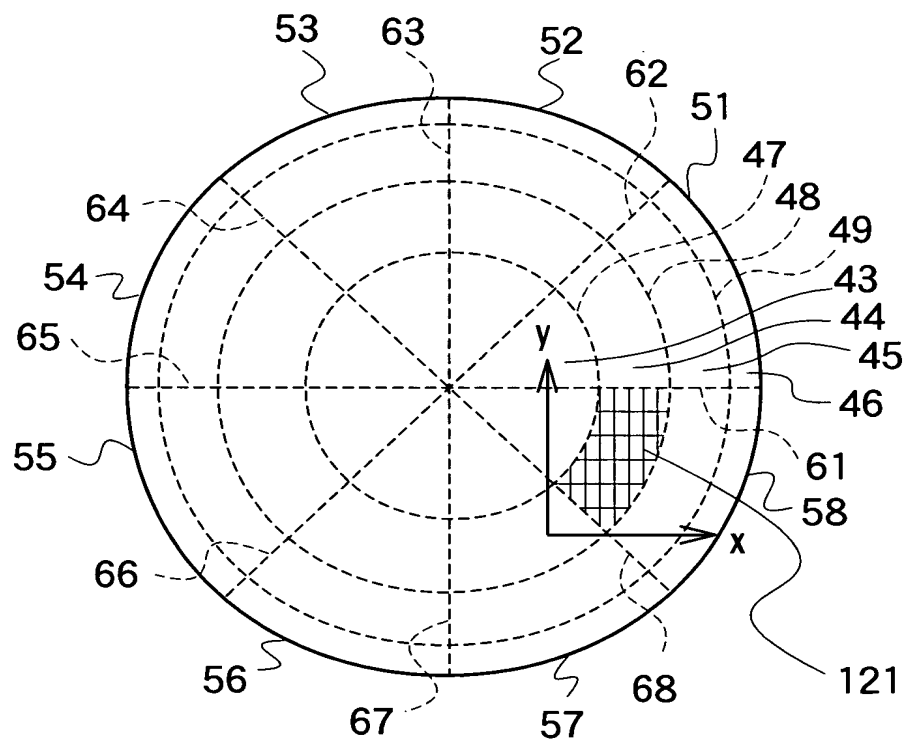


FIG. 14

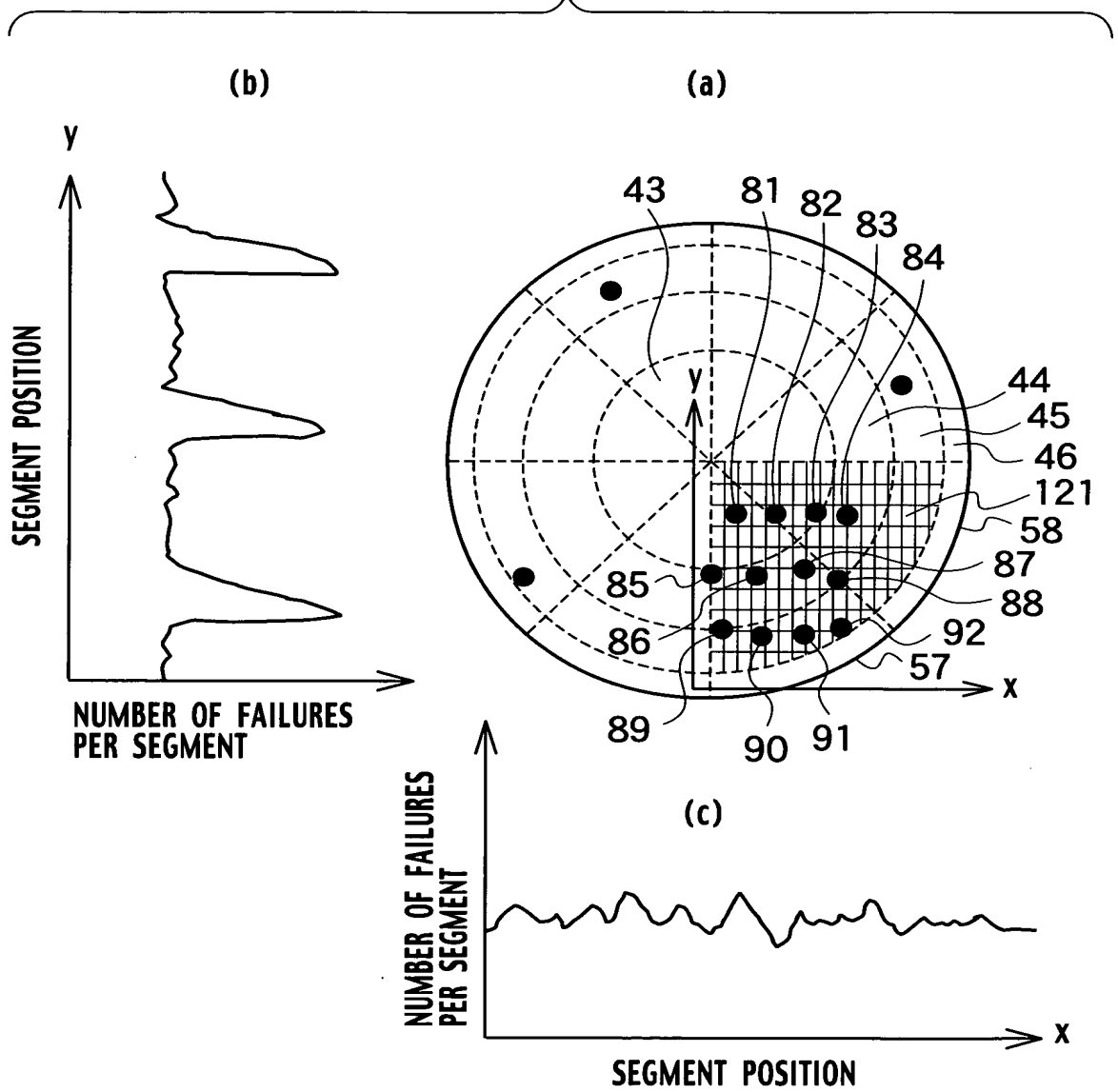


FIG. 15A

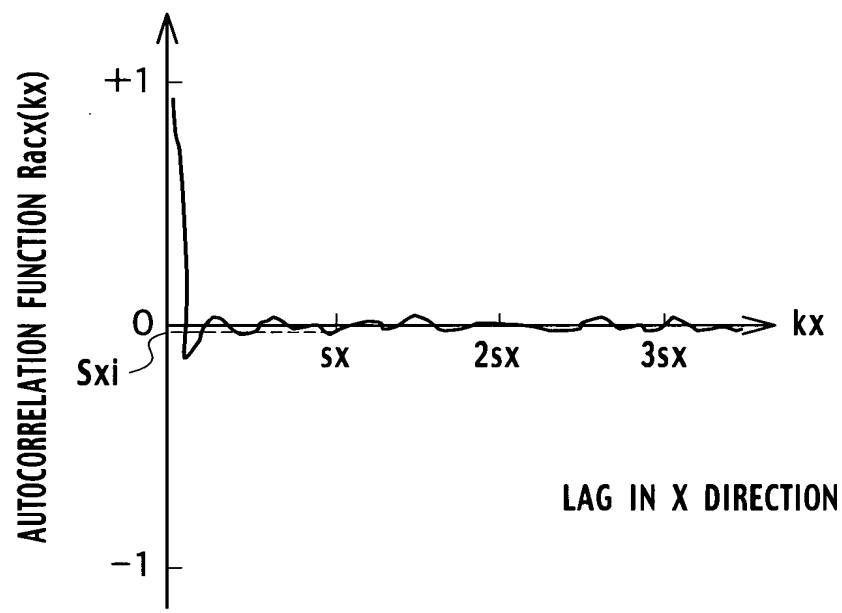


FIG. 15B

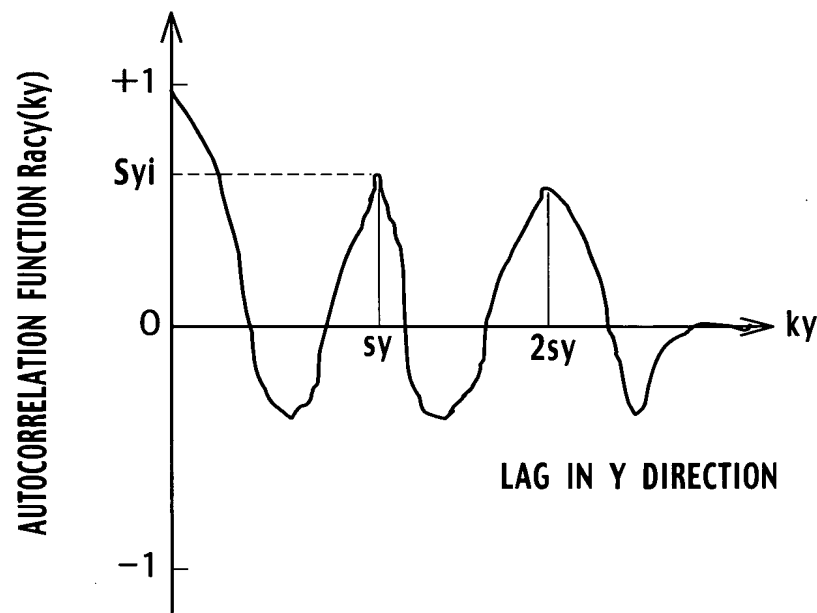


FIG. 16A

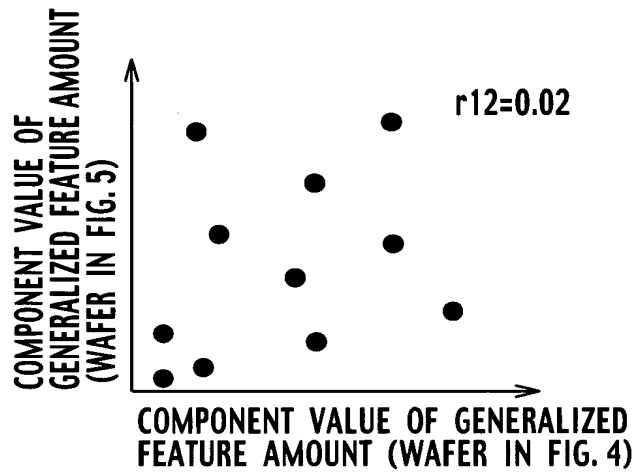


FIG. 16B

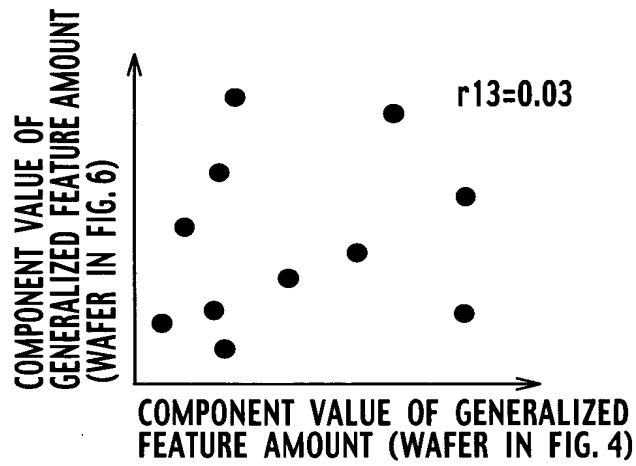


FIG. 16C

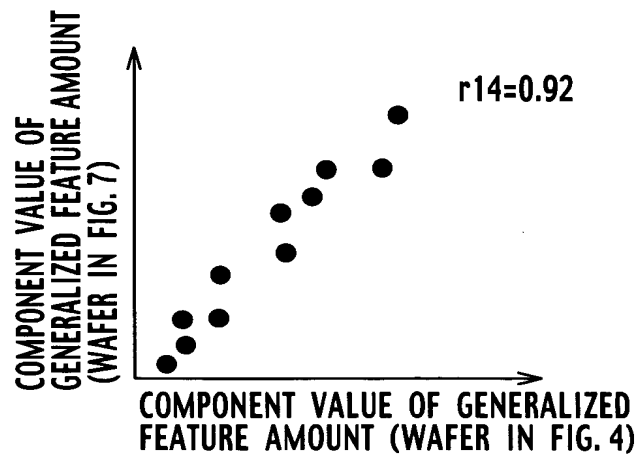


FIG. 17

	w1	w2	w3	w4
w1		×	×	○
w2	×		×	×
w3	×	×		×
w4	○	×	×	

FIG. 18

SIMILAR WAFER GROUP	NUMBER OF WAFERS	FAILURE CATEGORY	S1	S4	S2	S3
S1	2	C1		○	×	×
S4	2	C1	○		×	×
S2	1	C2	×	×		×
S3	1	C3	×	×	×	

FIG. 19

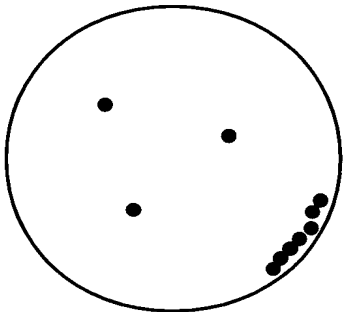
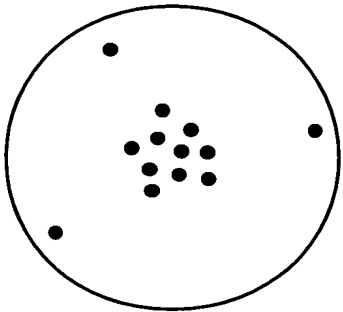
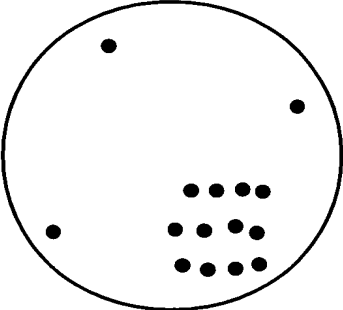
ORDER (CATEGORY)	NUMBER OF WAFERS	WAFER MAP
FIRST (C1)	2	 w1
SECOND (C2)	1	 w2
THIRD (C3)	1	 w3

FIG. 20

SIMILAR WAFER GROUP	NUMBER OF WAFERS	FAILURE CATEGORY	S10	S6	S2	S8	S9	S3
S10	23	C1		○	X	○	X	X
S6	19	C1	○		X	○	X	X
S2	17	C2	X	X		○	X	○
S8	15	C1	○	○	○		X	○
S9	14	C3	X	X	X	X		X
S3	12	C2	X	X	○	○	X	

FIG. 21

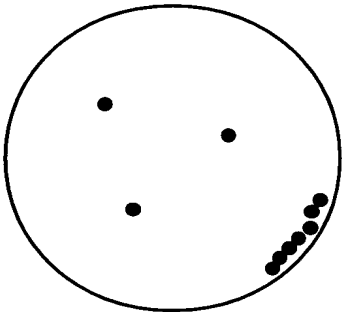
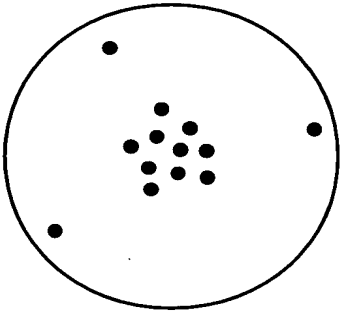
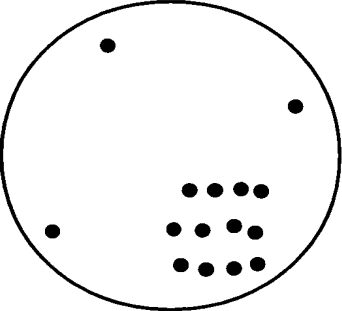
ORDER (CATEGORY)	NUMBER OF WAFERS	WAFER MAP
FIRST (C1)	31	
SECOND (C2)	22	
THIRD (C3)	16	

FIG. 22

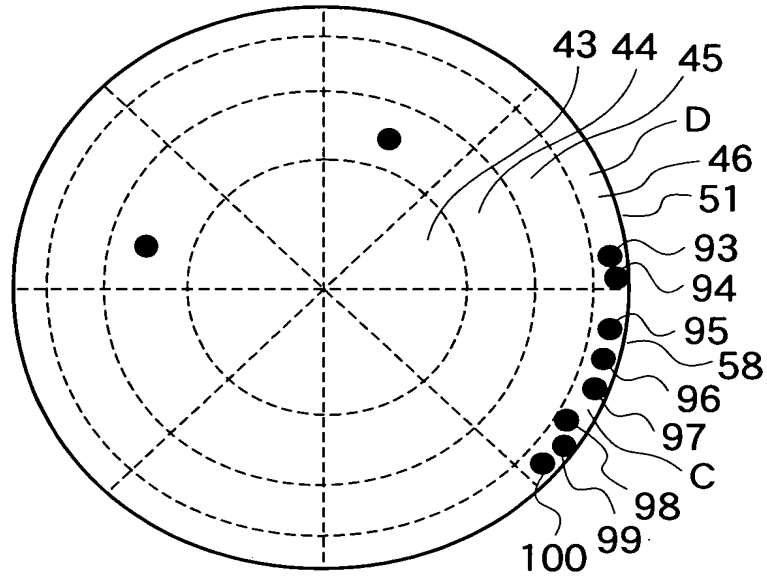


FIG. 23

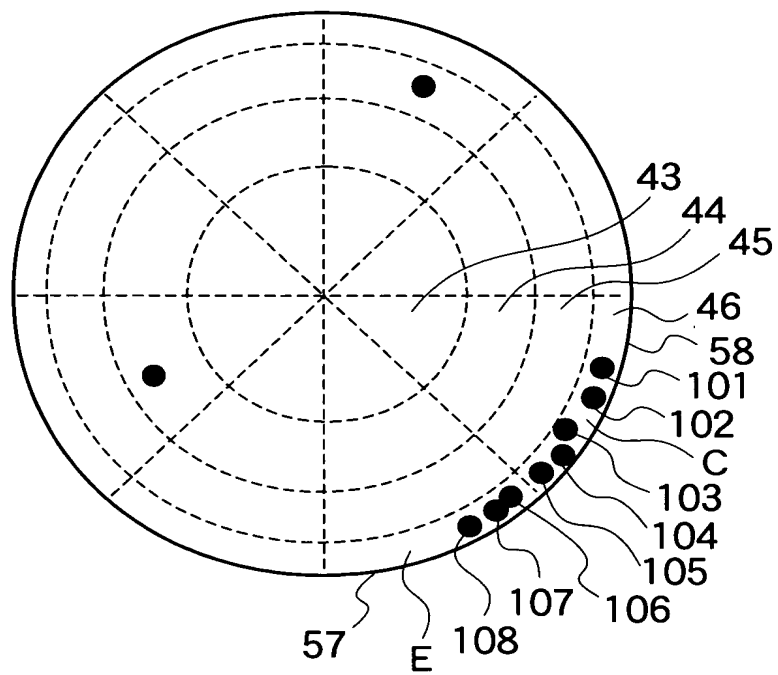


FIG. 24

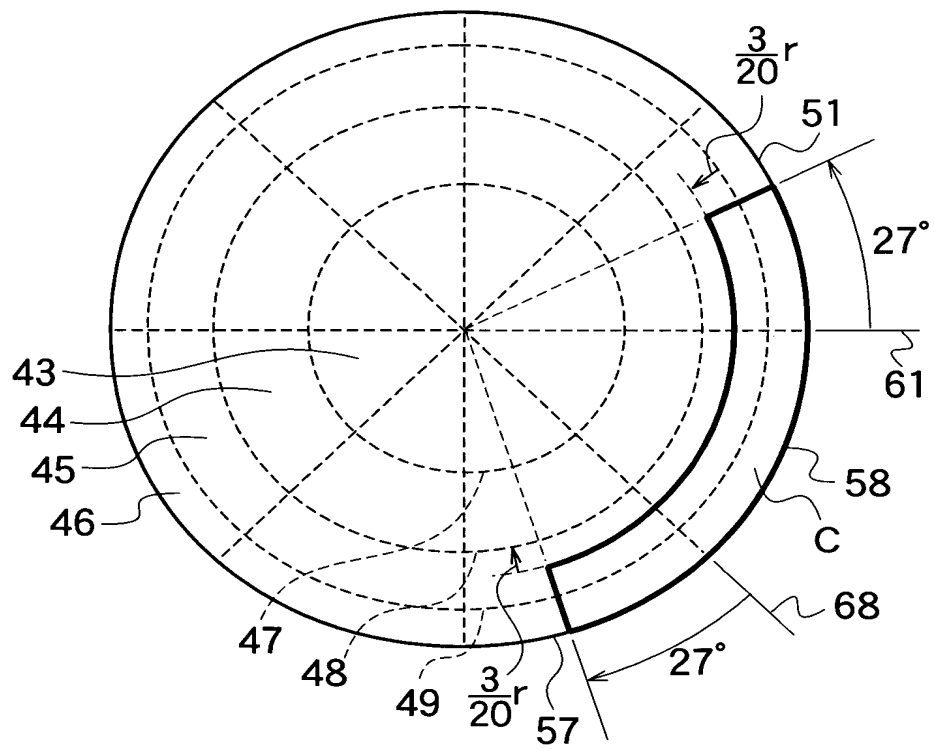


FIG. 25

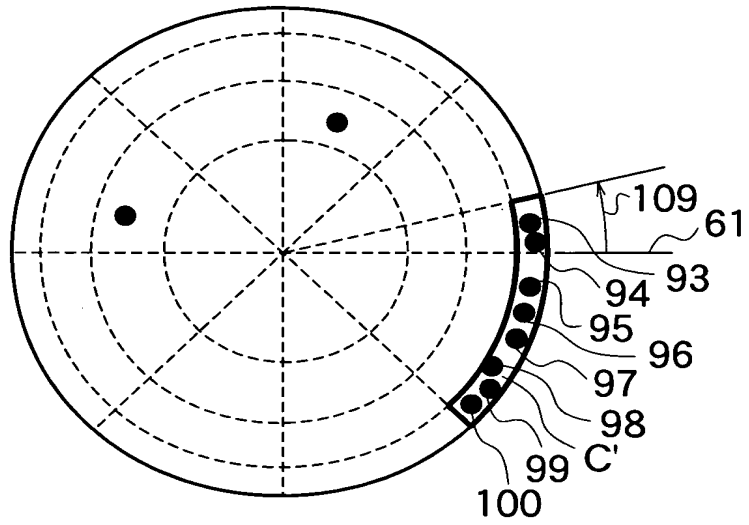


FIG. 26

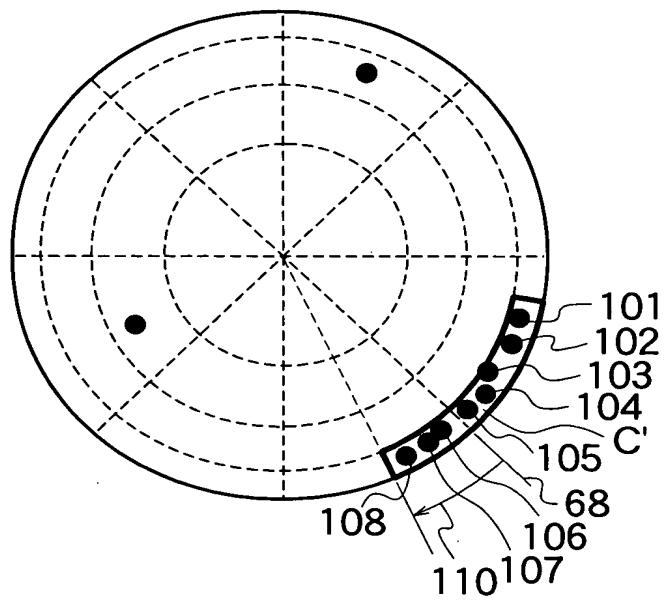
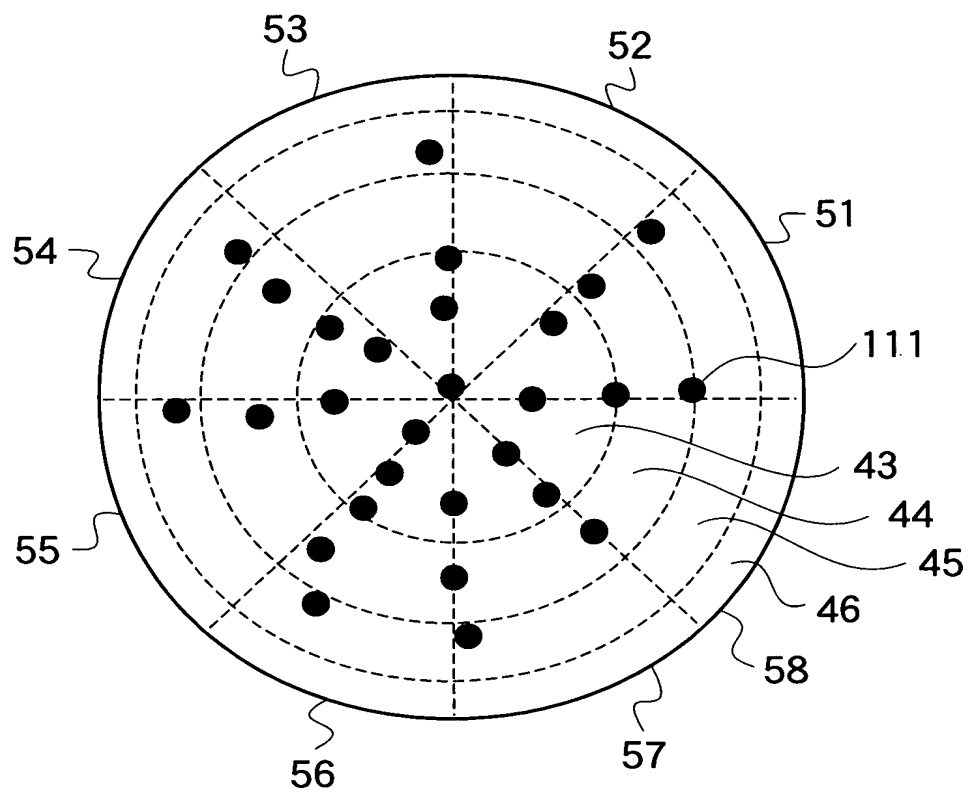


FIG. 27



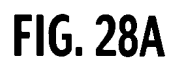


FIG. 30A

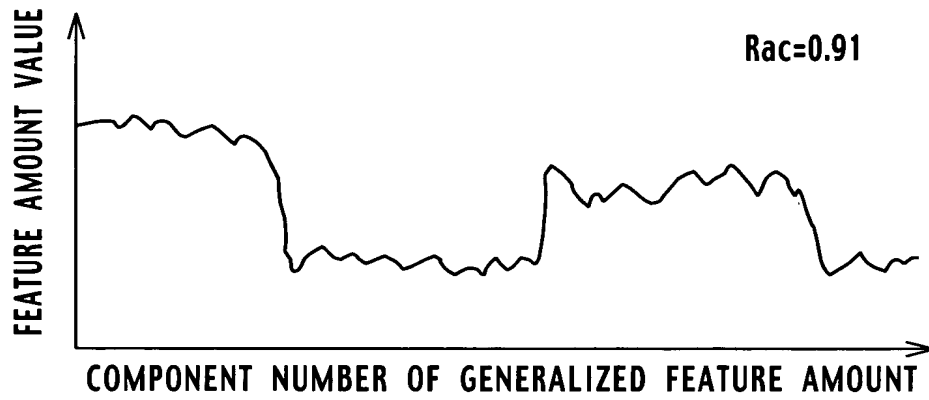


FIG. 30B

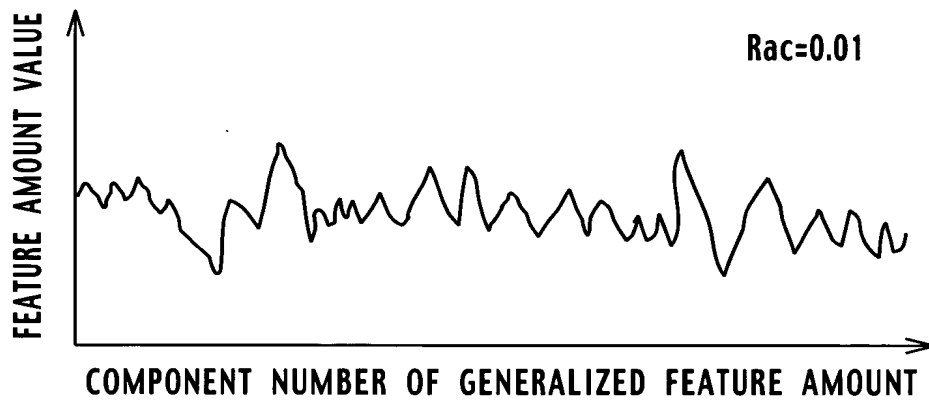


FIG. 30C

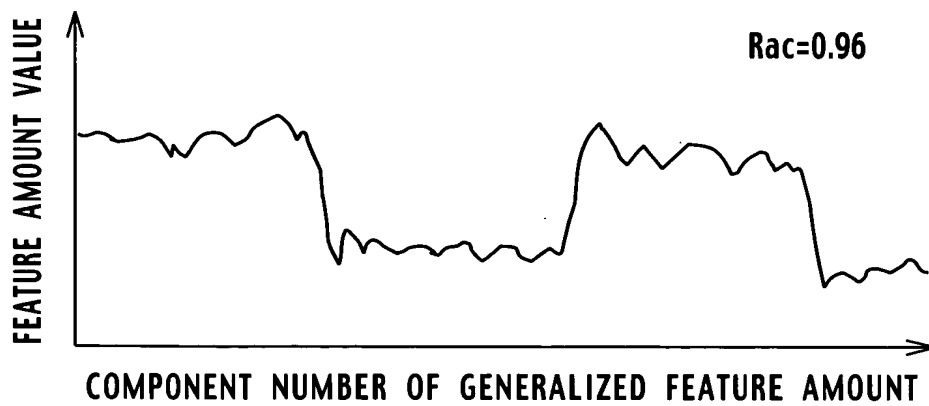


FIG. 31A

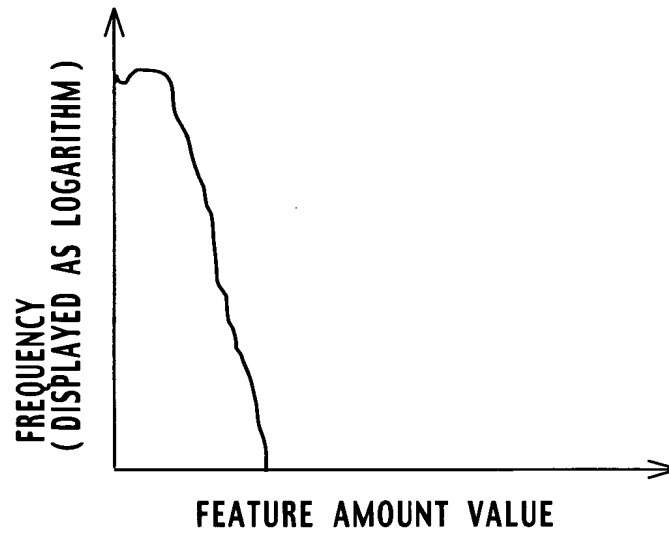


FIG. 31B

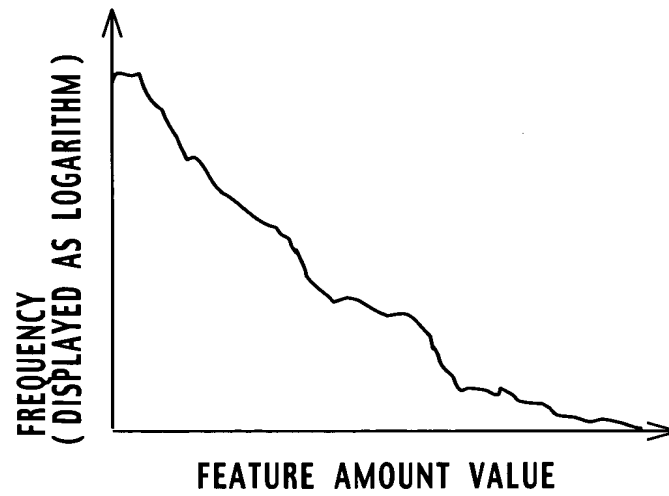


FIG. 32

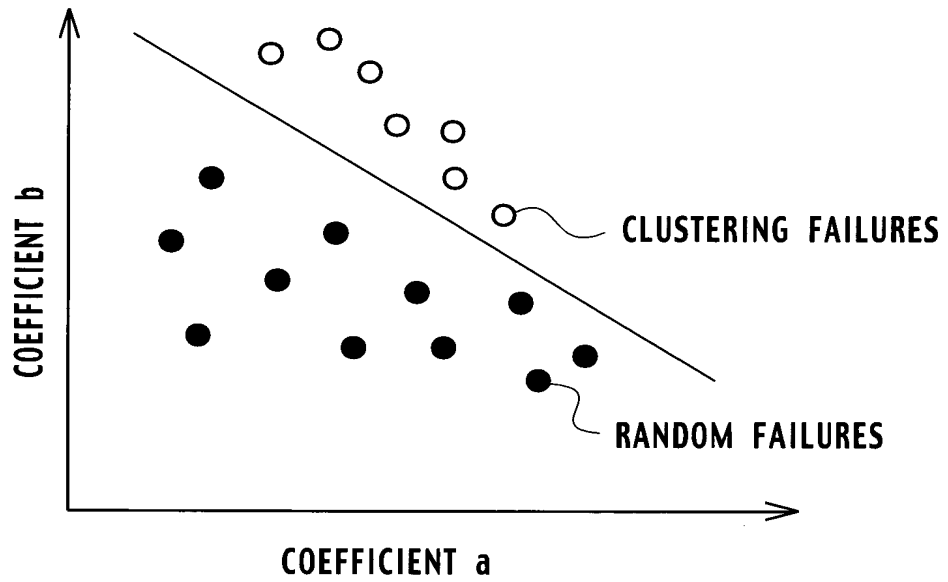


FIG. 33

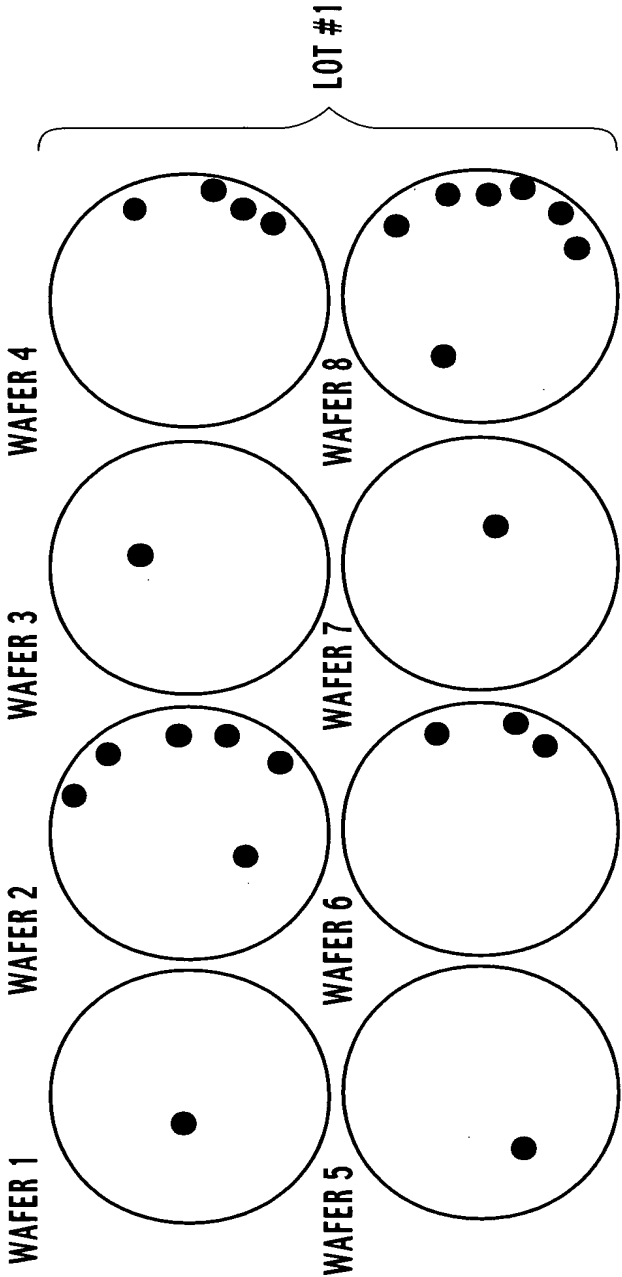


FIG. 34

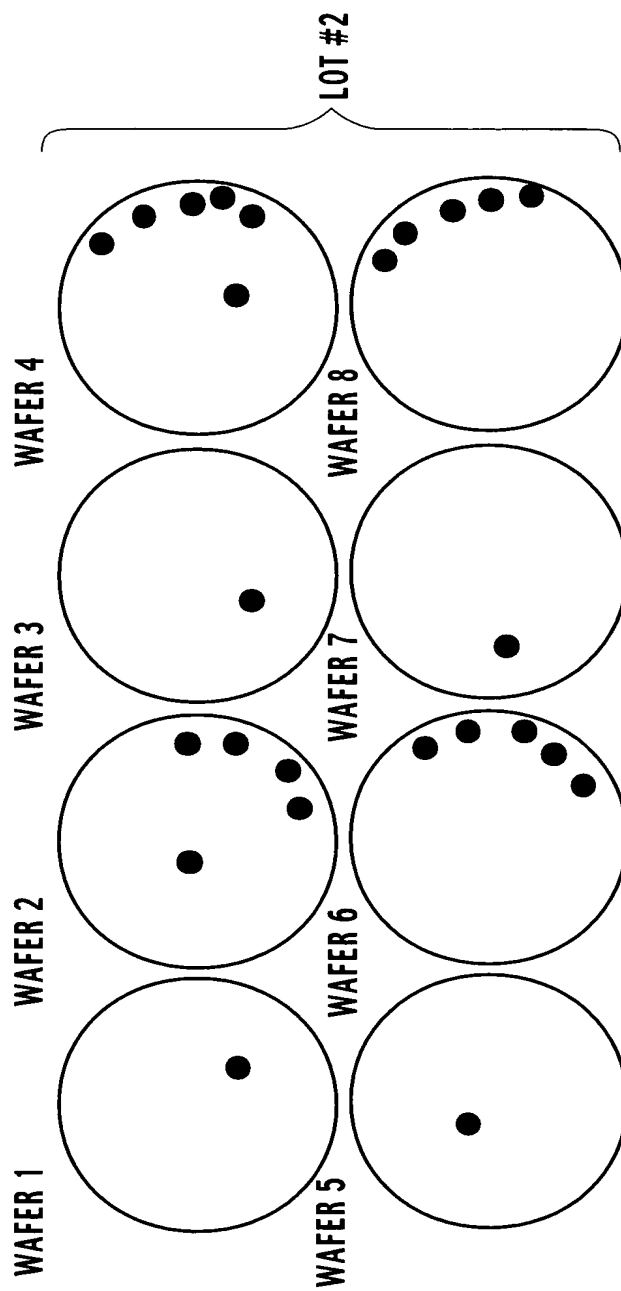


FIG. 35

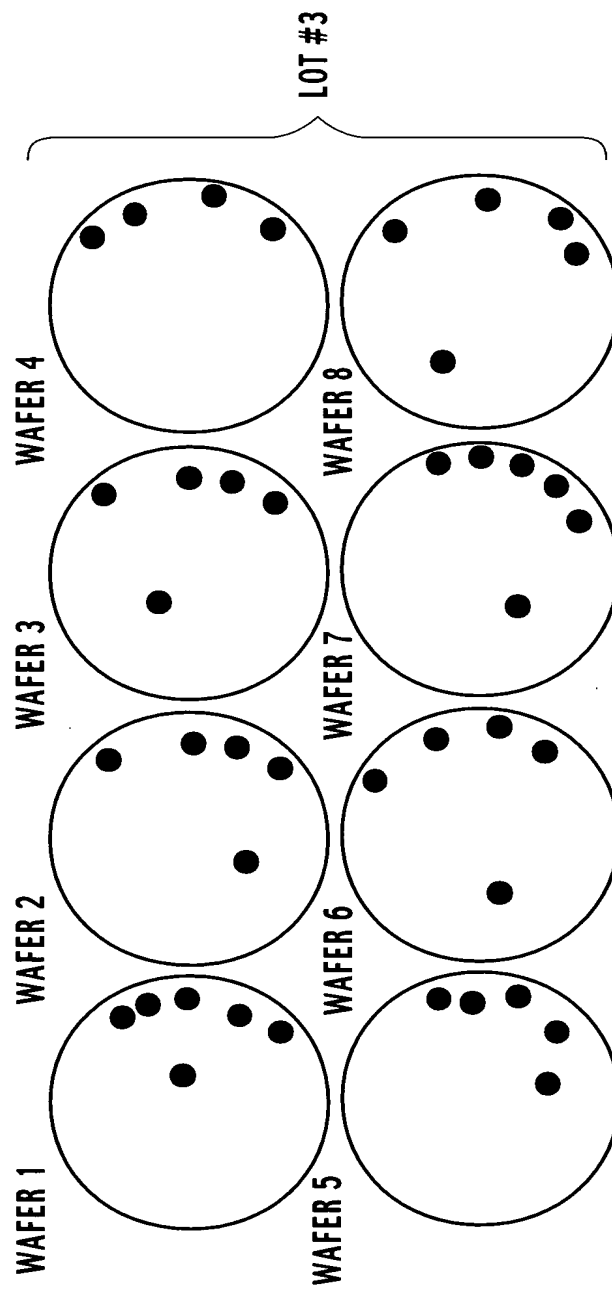


FIG. 36A

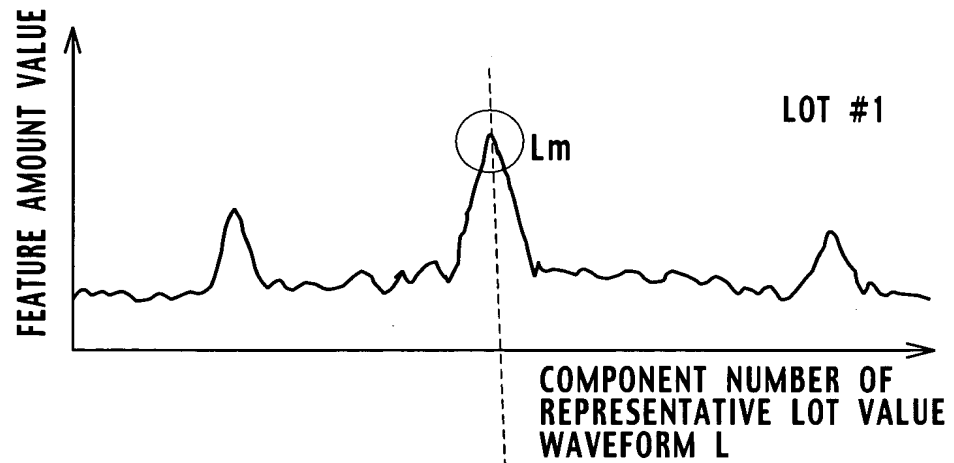


FIG. 36B

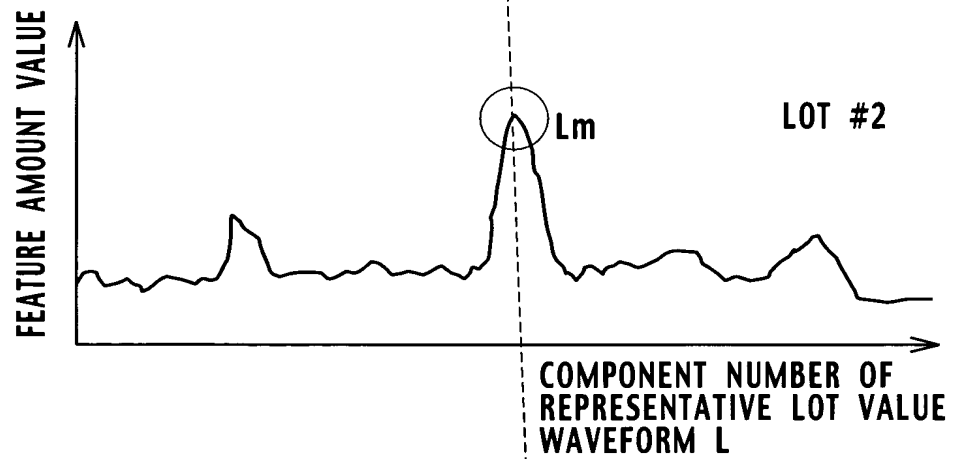


FIG. 36C

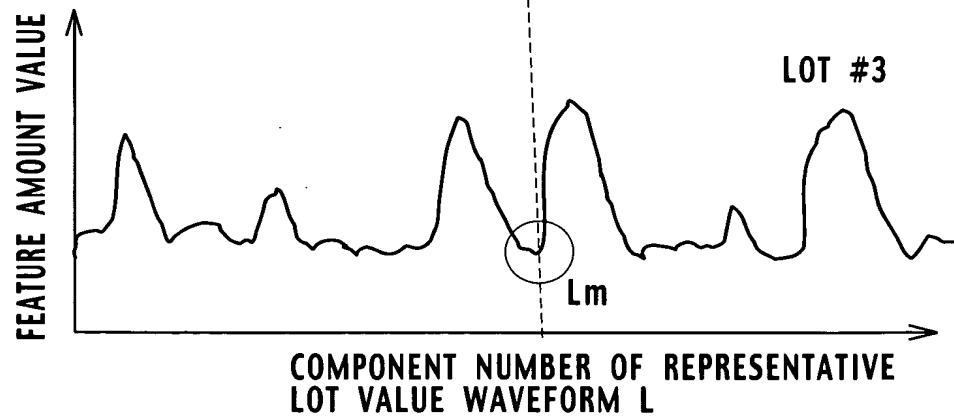


FIG. 37A

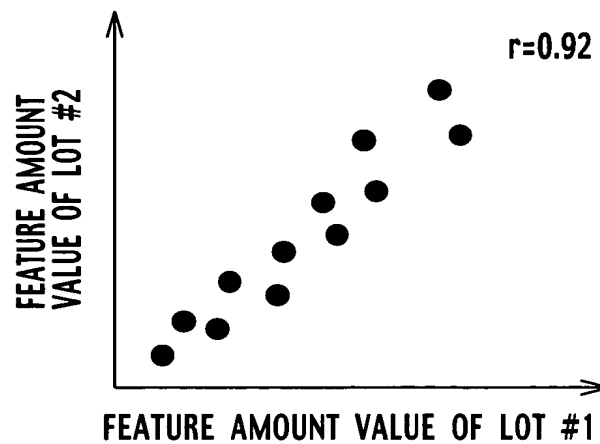


FIG. 37B

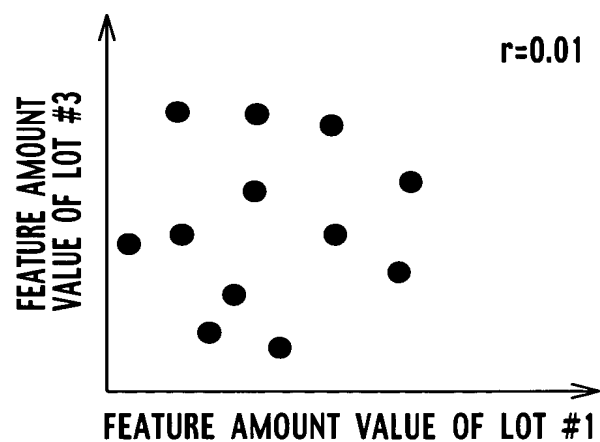


FIG. 37C

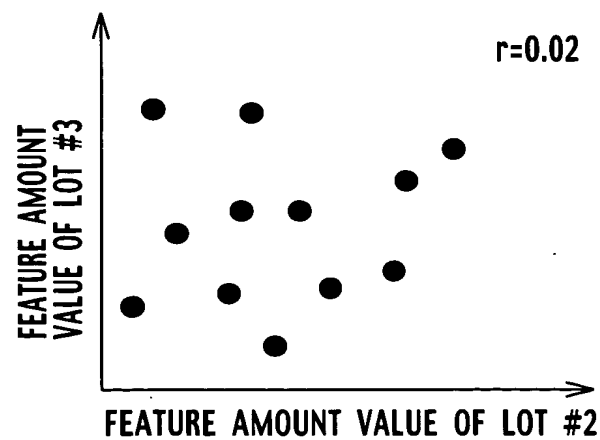


FIG. 38A

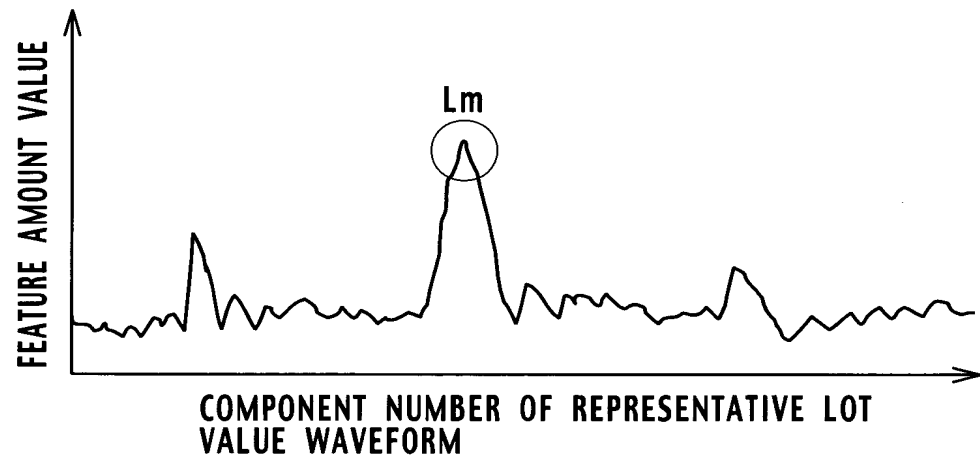


FIG. 38B

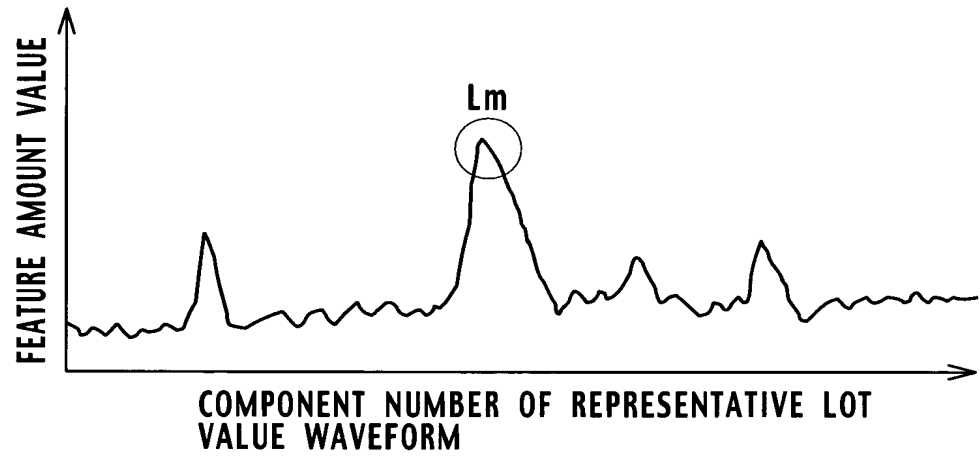


FIG. 38C

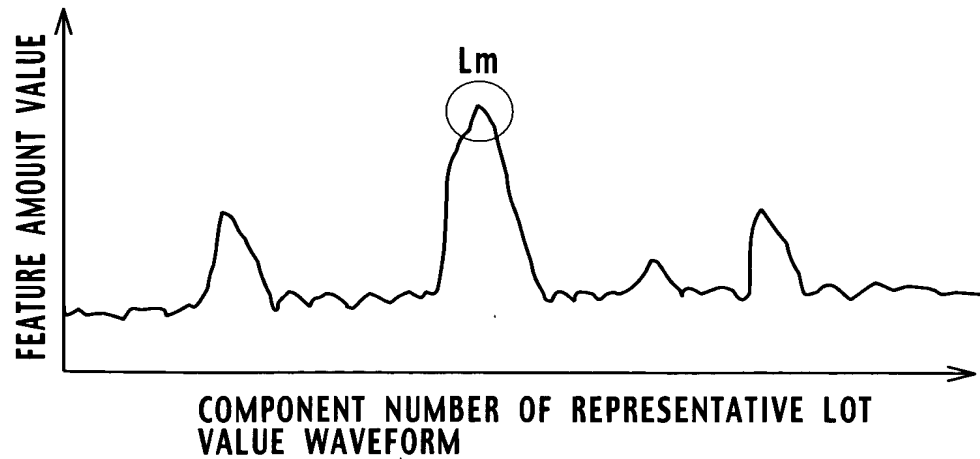


FIG. 39A

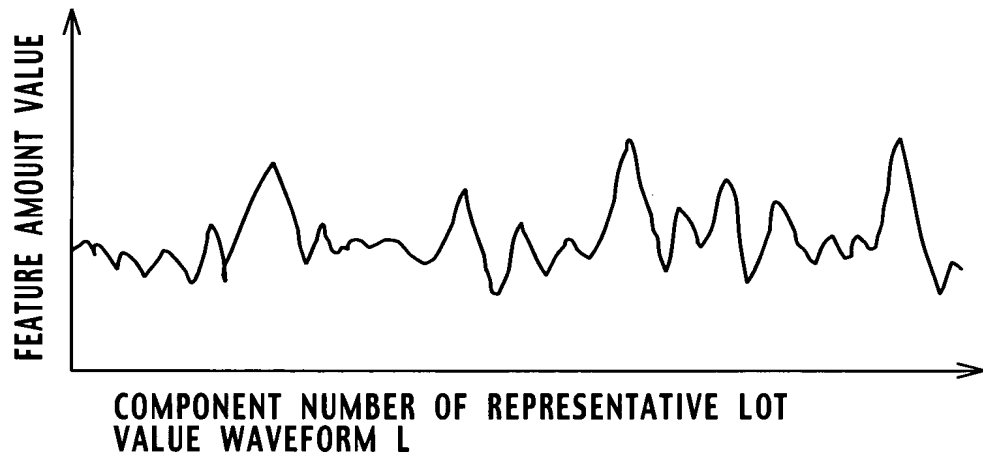


FIG. 39B

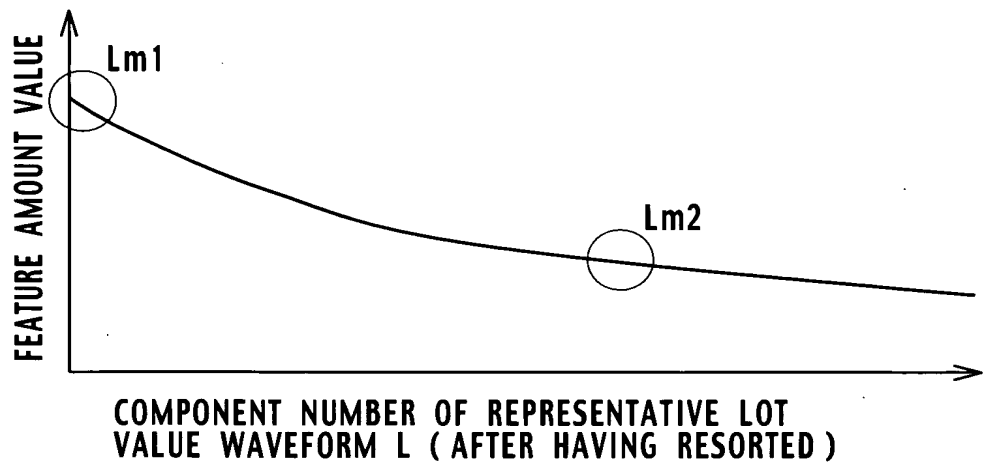


FIG. 40A

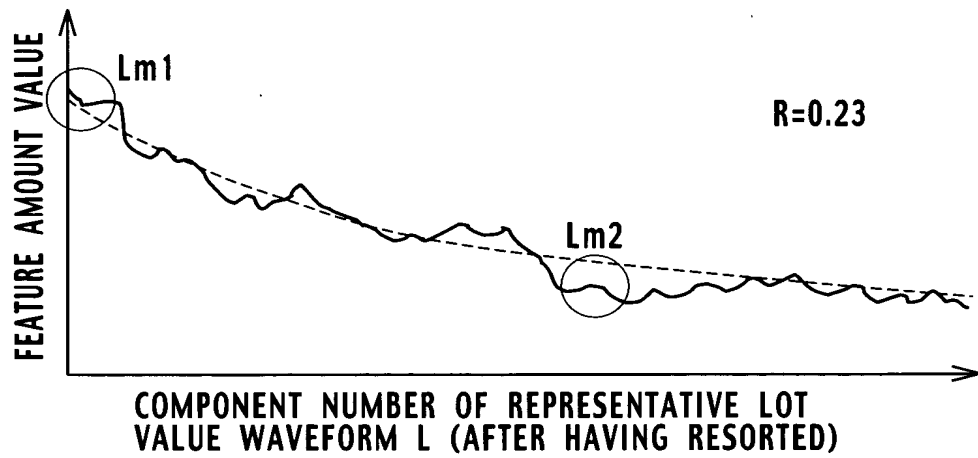


FIG. 40B

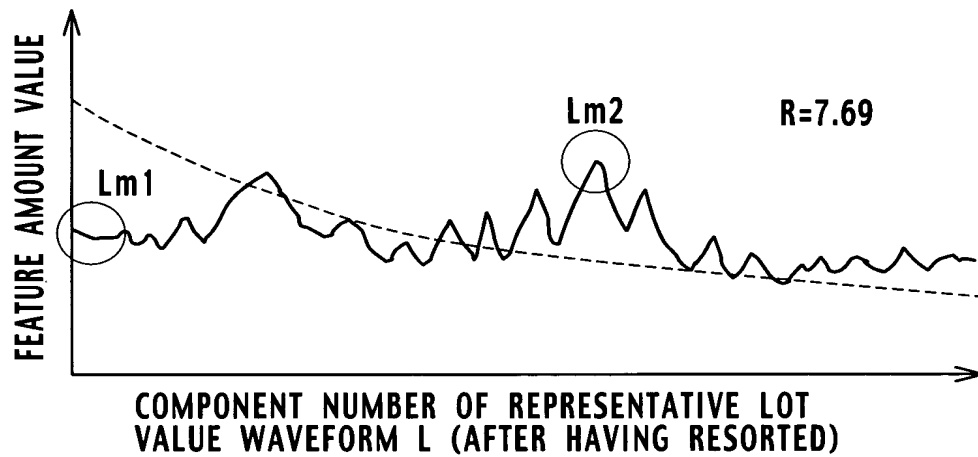


FIG. 40C

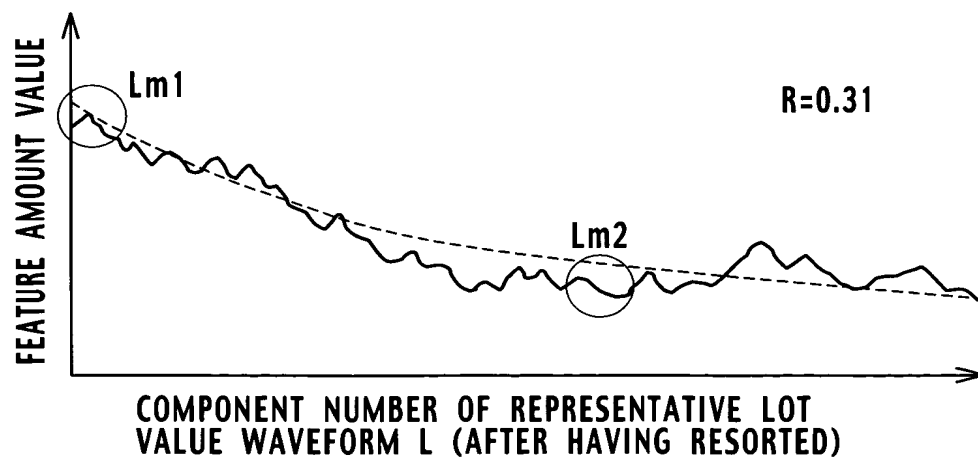


FIG. 41

	#4	#5	#6	#7
#4		○	×	○
#5	○		×	○
#6	×	×		×
#7	○	○	×	

FIG. 42

SIMILAR LOT SET	NUMBER OF LOTS	FAILURE CATEGORY	S4	S5	S7	S6
S4	3	G1		○	○	×
S5	3	G1	○		○	×
S7	3	G1	○	○		×
S6	1	G2	×	×	×	